



**TIME TABLE FOR BACHELORS DEGREE PROGRAMMES  
DEPARTMENT OF ELECTRONIC ENGINEERING**

F/QSP 11/04/05

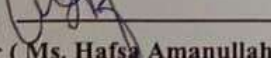
TIME TABLE FOR: FINAL YEAR (FALL SEMESTER-2024) BATCH: 2021  
EFFECTIVE DATE: 19-08-2024

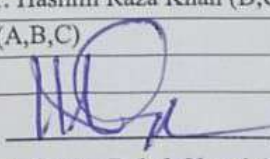
SECTION - A					Room No: 16
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday
08:30 - 09:20	VLSI LAB (Group-A) (Ms. Mariyum)-CC2	OB		NM	FYDP
09:20 - 10:10		OB		NM	
10:10 - 11:00		EE		OB	
11:00-11:20* 11:00 - 11:30	<b>Recess</b>				
11:20-12:10* 11:30 - 12:20	EE LAB (Group-A) (Ms. Sidra)-CC2  VLSI LAB (Group-B) (Ms. Mariyum)-CC1	EE	EE LAB (Group-C) (Ms. Arhum)-CC1 (Group-D) (Ms. Sidra)-CC2	VLSI	FYDP
12:10-13:00* 12:20 - 13:10		VLSI		VLSI	
13:00-14:30* 13:10 - 14:00	<b>Lunch/Prayer Time</b>				
14:30-15:20* 14:00 - 14:50	EE LAB (Group-B) (Ms. Sidra)-CC1	NM	VLSI LAB (Group-C) (Ms. Mariyum)-CC2 (Group-D) (Ms. Madiha)-CC2	EE	FYDP
15:20-16:10* 14:50 - 15:40					
15:40 - 16:30					

SECTION - B					Room No: 17
DAYS/ PERIODS	Monday	Tuesday	Wednesday(Room No.: 23)	Thursday	Friday
08:30 - 09:20	VLSI LAB (Group-A) (Ms. Mariyum)-CC2	NM		VLSI	FYDP
09:20 - 10:10		VLSI	EE	OB	
10:10 - 11:00		VLSI	EE	NM	
11:00-11:20* 11:00 - 11:30	<b>Recess</b>				
11:20-12:10* 11:30 - 12:20	EE LAB (Group-A) (Ms. Sidra)-CC2  VLSI LAB (Group-B) (Ms. Mariyum)-CC1	OB	EE LAB (Group-C) (Ms. Arhum)-CC1 (Group-D) (Ms. Sidra)-CC2	NM	FYDP
12:10-13:00* 12:20 - 13:10		OB		EE	
13:00-14:30* 13:10 - 14:00	<b>Lunch/Prayer Time</b>				
14:30-15:20* 14:00 - 14:50	EE LAB (Group-B) (Ms. Sidra)-CC1		VLSI LAB (Group-C) (Ms. Mariyum)-CC2 (Group-D) (Ms. Madiha)-CC2		FYDP
15:20-16:10* 14:50 - 15:40					
15:40 - 16:30					

Note: Friday is assigned for Final Year Design Project (FYDP) work.

Course Code	Course Title	Abbreviation	Course/Practical Teacher Name & Sections
MG-482	Organizational Behavior	OB	TBA
MT-442	Numerical Method	NM	TBA
EL-408	VLSI System Design	VLSI	Ms. Maryam Jamshed (A) , Dr. Hashim Raza Khan (B,C)
EL-421	Embedded Electronics	EE	Ms Hafsa (A,B,C)
EL-401	Electronics Engineering Project	FYDP	

Reviewed By:   
Class Advisor ( Ms. Hafsa Amanullah)  
Date: 21-07-24

Approved By:   
Chairman ( Dr. Ghous Baksh Narejo)  
Date: 01/08/2024



**TIME TABLE FOR BACHELORS DEGREE PROGRAMMES  
DEPARTMENT OF ELECTRONIC ENGINEERING**

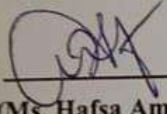
F/QSP 11/04/05


**TIME TABLE FOR: FINAL YEAR (FALL SEMESTER-2024) BATCH: 2021  
EFFECTIVE DATE: 19-08-2024**

SECTION - C DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Room No: 18 Friday
08:30 - 09:20	<b>VLSI LAB</b> (Group-A) (Ms. Mariyum)-CC2	VLSI		OB	FYDP
09:20 - 10:10		NM		EE	
10:10 - 11:00		NM		EE	
11:00-11:20* 11:00 - 11:30	<b>Recess</b>				
11:20-12:10* 11:30 - 12:20	<b>EE LAB</b> (Group-A) (Ms. Sidra)-CC2 <b>VLSI LAB</b> (Group-B) (Ms. Mariyum)-CC1	VLSI	<b>EE LAB</b> (Group-C) (Ms. Arhum)-CC1 (Group-D) (Ms. Sidra)-CC2	OB	FYDP
12:10-13:00* 12:20 - 13:10		VLSI		OB	
13:00-14:30* 13:10 -14:00	<b>Lunch/Prayer Time</b>				
14:30-15:20* 14:00 - 14:50	<b>EE LAB</b> (Group-B) (Ms. Sidra)-CC1	EE	<b>VLSI LAB</b> (Group-C) (Ms. Mariyum)-CC2 (Group-D) (Ms. Madiha)-CC2	NM	FYDP
15:20-16:10* 14:50 - 15:40					
15:40 - 16:30					

Note: Friday is assigned for Final Year Project (FYP) work.

Course Code	Course Title	Abbreviation	Course/Practical Teacher Name & Sections
MG-482	Organizational Behavior	OB	TBA
MT-442	Numerical Method	NM	TBA
EL-408	VLSI System Design	VLSI	Ms. Maryam Jamshed (A), Dr. Hashim Raza Khan ( B, C)
EL-421	Embedded Electronics	EE	Ms. Hafsa (A,B,C)
EL-401	Electronics Engineering Project	FYDP	

Reviewed by:   
Class Advisor (Ms. Hafsa Amanullah)  
Date: 31-07-24

Approved by:   
Chairman (Dr. Ghaus Baksh Narejo)  
Date: 1/8/2024