



**TIME TABLE FOR BACHELORS DEGREE PROGRAMMES  
DEPARTMENT OF ELECTRONIC ENGINEERING**

F/QSP 11/04/05

**DEPARTMENT OF ELECTRONIC ENGINEERING  
TIME TABLE FOR: FINAL YEAR (FALL SEMESTER-2023) BATCH: 2020  
EFFECTIVE DATE: 20-11-2023**

SECTION - A	Room No: 16				
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday
08:30 - 09:20		OB	VLSI LAB (Group-A) (Ms. Maryam)- CC2	NM	FYP
09:20 - 10:10		OB		NM	
10:10 - 11:00		OB		VLSI	
11:00-11:20* 11:00 - 11:30	<b>Recess</b>				
11:20-12:10* 11:30 - 12:20	Emb. EL LAB (Group-C) (Ms. Hafsa)-CC2 SSD LAB (Group-D) (Ms. Hira)-CC1	Emb.EL	Emb. EL LAB (Group-A) (Ms. Hafsa)-CC1 (Group-B) (Mr. Ubaidullah)-CC2	Emb.EL	FYP
12:10-13:00* 12:20 - 13:10		VLSI		VLSI	
13:00-14:30* 13:10 - 14:00	<b>Lunch/Prayer Time</b>				
14:30-15:20* 14:00 - 14:50	VLSI LAB (Group-C) (Ms. Maryam)-CC2, (Group-D) (Mr. Ubaidullah)-CC1	NM	VLSI LAB (Group-B) (Ms. Maryam)- CC2	Emb.EL	FYP
15:20-16:10* 14:50 - 15:40					
15:40 - 16:30					

SECTION - B	Room No: 17				
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday
08:30 - 09:20		Emb. EL	VLSI LAB (Group-A) (Ms. Maryam)-CC2	Emb. EL	FYP
09:20 - 10:10		Emb. EL		VLSI	
10:10 - 11:00		VLSI		VLSI	
11:00-11:20* 11:00 - 11:30	<b>Recess</b>				
11:20-12:10* 11:30 - 12:20	Emb. EL LAB (Group-C) (Ms. Hafa)-CC2 SSD LAB (Group-D) (Ms. Hira)-CC1	OB	Emb. EL LAB (Group-A) (Ms. Hafsa)-CC1 (Group-B) (Mr. Ubaidullah)-CC2	OB	FYP
12:10-13:00* 12:20 - 13:10		NM		OB	
13:00-14:30* 13:10 - 14:00	<b>Lunch/Prayer Time</b>				
14:30-15:20* 14:00 - 14:50	VLSI LAB (Group-C) (Ms. Maryam)-CC2, (Group-D) (Mr. Ubaidullah)-CC1		VLSI LAB (Group-B) (Ms. Maryam) -CC2	NM	FYP
15:20-16:10* 14:50 - 15:40				NM	
15:40 - 16:30					

Note: Friday is assigned for Final Year Project (FYP) work.

Course Code	Course Title	Abbreviation	Course/Practical Teacher Name & Sections
MG-482	Organizational Behavior	OB	Dr. Faheem Akhtar
MT-442	Numerical Method	NM	Dr. Fahim Raees
EL-408	VLSI System Design	VLSI	Ms. Maryam Jamshed (A) , Dr. Hashim Raza Khan (B,C)
EL-421	Embedded Electronics	Emb. EL	Ms Hafsa (A,B)
EL-401	Electronics Engineering Project	FYDP	

Reviewed By: \_\_\_\_\_

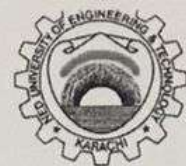
Class Advisor ( Dr. Sadia Muniza Faraz)

Date: 17-11-2023

Approved By: \_\_\_\_\_

Ag. Chairman ( Dr. M. Imran Aslam)

Date: 20/11/2023



TIME TABLE FOR BACHELORS DEGREE PROGRAMMES  
DEPARTMENT OF ELECTRONIC ENGINEERING


F/QSP 11/04/05

DEPARTMENT OF ELECTRONIC ENGINEERING  
TIME TABLE FOR: FINAL YEAR (FALL SEMESTER-2023) BATCH: 2020  
EFFECTIVE DATE: 20-11-2023


SECTION - C	Room No: 18				
DAYS/ PERIODS	Monday	Tuesday	Wednesday	Thursday	Friday
08:30 - 09:20		VLSI	VLSI LAB (Group-A) (Ms. Maryam)-CC2	OB	FYP
09:20 - 10:10		VLSI		OB	
10:10 - 11:00		SSD		SSD	
11:00-11:20* 11:00 - 11:30					
11:20-12:10* 11:30 - 12:20	Emb. EL LAB (Group-C) (Ms. Hafsa)-CC2 SSD LAB (Group-D) (Ms. Hira)-CC1	SSD	Emb. EL LAB (Group-A) (Ms. Hafsa)-CC1 (Group-B) (Mr. Ubaidullah)-CC2	VLSI	FYP
12:10 -13:00* 12:20 - 13:10		NM			
13:00-14:30* 13:10 -14:00	<b>Lunch/Prayer Time</b>				
14:30- 15:20* 14:00 - 14:50	VLSI LAB (Group-C) (Ms. Maryam)-CC2, (Group-D) (Mr. Ubaidullah)-CC1	OB	VLSI LAB (Group-B) (Ms. Maryam)-CC2	NM	FYP
15:20- 16:10* 14:50 - 15:40				NM	
15:40 - 16:30					

Note: Friday is assigned for Final Year Project (FYP) work.

Course Code	Course Title	Abbreviation	Course/Practical Teacher Name & Sections
MG-482	Organizational Behavior	OB	Dr. Faheem Akhtar
MT-442	Numerical Method	NM	Dr. Fahim Raees
EL-408	VLSI System Design	VLSI	Ms. Maryam Jamshed ( A), Dr. Hashim Raza Khan ( B, C)
EL-433	Solid State Devices	SSD	Dr. Ghous Buksh (C)
EL-401	Electronics Engineering Project	FYDP	

Reviewed by:   
Class Advisor (Dr. Sadia Muniza Faraz)

Date: 17-11-2023

Approved by:   
Ag. Chairman (Dr. M. Imran Aslam)

Date: 20/11/2023