

Ayesha Akhtar

Email: ayesha@neduet.edu.pk

EDUCATION

- N.E.D University of Engineering & Technology (June 2008 –Dec.2010)
M.E in MSD (Micro System Design) from Electronic Department.
- N.E.D University of Engineering & Technology (Jan 2004-Dec 2007)
B.E. Electronic Engineering.
- St. Joseph's Govt. College for Women (2001-2003)
H.S.C -Pre Engineering
- Habib Girls' School (2001)
S.S.C

PROFESSIONAL EXPERIENCE

- Working as Lecturer in NED University of Engg. & Technology from Jan. 2010 to date.
- Worked in National University of Computer and Emerging Sciences-FAST as Lecturer in Department of Electrical Engineering from April 2008 to Dec. 2009.
- CAA –Civil Aviation Authority (2006)
Worked as an internee for 40 days.
- PIA Engineering (2006)
Worked as an internee for 15 days.

COURSES TAUGHT:

- Electronic Devices & Circuit.
- Digital Electronics

PRACTICALS CONDUCTED:

- Electronics-II
- Wave Propagation & Antenna Theory
- Engineering Workshop
- Digital Electronics
- Analog Integrated Circuit
- Power Electronics

FINAL YEAR PROJECT

- Design of Internal UPS for PCs
This UPS is embedded in the CPU. It does not only supply power to PC during power failure but also saves the data automatically using hibernation mode and then shuts down the system immediately. When power restores hibernation mode provides faster restoration of data than hard reboot which takes place when a typical UPS is used.

TECHNICAL SKILLS

- Turbo C, Verilog HDL, MS Office, AutoCad, Multisim, Matlab, Orcad & Dip Trace (PCB Designing) PCB123, Microwind, Modelsim, Xilinx, Mentor Graphics (IC Studio).

MEMBERSHIPS:

- Registered Engineer from Pakistan Engineering Council (PEC)